



2152

JPW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Chiazzese
Serial No. : 09/920,423
Title : Synchronized Sampling on a Multiprocessor Backplane Via a Broadcast Timestamp
Filed : August 1, 2001
Group Art Unit : 2152
Examiner : Unknown
Attorney Docket No. : 560043670134

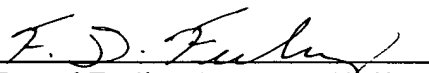
STATUS INQUIRY

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please advise as to the status of this application and provide an estimated date for examination.

Respectfully submitted,


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Date: June 9, 2004

I hereby certify that this correspondence is being deposited today with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on June 10, 2004.

By: 